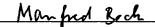
Specifically certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

28791 EN



Date: May 13, 2005

<u>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</u>

Applic. No.

10/090,289

Confirmation No: 6619

Applicant

Johann Winderl March 4, 2002

Filed Title

Electric Component with Stacked Semiconductor Chips

Art Unit

2829

Examiner Docket No.

Luan C. Thai MAS-FIN-153

Customer No. :

24131

UNDER 37 C.F.R. 1.97(i)

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States patent No. 6,191,370 B1 (Oh), dated February 20, 2001;

German Examination Report dated December 16, 2004.

It is believed that the enclosed prior art is less pertinent than the prior art previously submitted and cited by the Examiner. Kindly place the references in the Patent Office file wrapper.

Respectfully submitted,

For Applicant

Date: May 13, 2005

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MANFRED BECK REG. NO. 45,342

ann Winderl			
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